

U.S. Patent Application No. 09/904,319  
Reply to Office Action dated December 9, 2005

PATENT  
450100-03342

**IN THE SPECIFICATION**

Please amend the Specification as follows:

3-13  
On page 18, lines ~~10-11~~:

In step 57, the system controller 19 judges whether the recording monitor button or the return button is operated via the user interface. If the recording monitor button is judged operated, step 58 is reached. In step 58, the system controller 19 causes the post-processor 15 to give a split-screen display composed of a recording monitor screen and a playback screen as shown in FIG. 6. At this point, the ~~recording monitor screen~~ playback screen is made to appear slightly larger than the ~~playback screen~~ recording monitor screen. Alternatively, the recording monitor screen being selected may be highlighted in the regions of its frame.

On page 25, lines 17-18:

If recording is designated in that state, step 58 in ~~FIG. 5~~ Fig. 2 or step 6 in FIG. 9 is reached, and the subsequent steps are repeated.

18-24 to page 21, line 1:  
On page 20, lines ~~21-23~~:

In step 2, the system controller 19 checks to see if an end of recording is designated through the user interface or if a predetermined recording end time is reached. If the end of recording is judged designated, the system controller 19 goes to step 3 and checks to see if playback is designated via the user interface. Otherwise the system controller 19 terminates recording and enters standby mode. ~~Otherwise the system controller 19 goes to step 3 and checks to see if playback is designated via the user interface.~~

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On page 21, lines ~~23-24~~ <sup>19-24 to page 22, line 1:</sup>

*AB*  
*7/16/07*

In step 5, the system controller 19 judges whether the recording monitor screen is selected via the user interface. If the recording monitor screen is judged selected, the system controller 19 goes to step 6 and causes the post-processor 15 to make the playback screen ~~recording monitor screen larger than the playback screen~~ recording monitor screen, as illustrated in FIG. 6.

On pages 11-12:

The video signal recording and reproducing apparatus shown in FIG. 1 comprises: a tuner 1 for receiving RF signals input via an antenna; a switch 2 for receiving a composite input from the outside and a composite signal from the tuner 1 and selectively outputting one of the two inputs; a YC separation circuit 3 that subjects the composite signal coming from the switch 2 to YC separation; a NTSC decoder 4 which converts YC signals prior to pre-processing; a pre-processor 5 for effecting various video signal processes such as pre-filtering of luminance and color difference signals supplied by the YC separation circuit 3; an encoder 6 for generating an elementary stream by subjecting video signals coming from the pre-processor 5, e.g. to MPEG compression coding; an ECC modulation circuit 8 which adds an error correcting code to the video signals compression-coded by the encoder 6 and which modulates the compression-coded video signals supplemented with the error correcting code; a recording circuit 9 for converting the signals coming from the ECC modulation circuit 8 into recording signals; and a read/write head 10 that writes and reads signals to and from a storage medium 22 in accordance with the recording signals coming from the recording circuit 9.